AMENDMENTS TO THE CLAIMS

Claims 1-30 are pending in the instant application. Claims 1-8, 10-18, 20-28 and 30 have been amended. Claims 2-10, 12-20 and 22-30 depend directly or indirectly from independent claims 1.11 and 21, respectively.

The Applicant requests reconsideration of the claims in view of the following claim amendments and remarks.

Listing of claims:

 (Currently Amended) A method for communicating information in a server, the method comprising:

receiving at a common switch, at least one packet from a first blade server of a plurality of blade servers, wherein said at least one packet is designated for at least a second blade server of said plurality of blade servers, and at least two of whichwherein said first blade server and said at least a second blade server are coupled to [[a]] said common switch via a common bus;

determining at least one identifier associated with at least a second blade server based on at least a portion of said received at least one packet; and determining at least a first identifier identifying said common switch, a second identifier identifying said first blade server, and at least a third identifier identifying said second blade server, wherein

said first, second and third identifiers are located within a header portion of said

received at least one packet; and

routing via said common switch, based on said determined at least one identifier,

at least a portion of said at least one received packet to at least said second blade

server, based on said determined first, second and third identifiers from said header

portion of said at least one received packet.

2. (Currently Amended) The method according to claim 1, comprising

transferring said at least a header portion of said at least one received packet to said at

least said second blade server via said common switch.

3. (Currently Amended) The method according to claim 1, wherein said

common switch comprises a switch blade coupled to said common bus, and wherein

said switch blade controls said routing of at least a said header portion of said received

packet.

4. (Currently Amended) The method according to claim [[3]]1, comprising

determining at least one identifier of said switch bladewherein said common bus

comprises a common backplane.

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 (Currently Amended) The method according to claim [[4]]1, eomprising determining at least one identifier of said first blade-serverwherein said common switch

comprises a bus transceiver and a bus controller.

6. (Currently Amended) The method according to claim [[5]]1, wherein each

of said first, second, and third identifiers identifier of said first blade server, said

identifier of said second blade server, and said identifier of said switch blade each

comprises one or both of a MAC address and/or an IP address.

7. (Currently Amended) The method according to claim 1, comprising:

acquiring at least onesaid second identifier of said first blade server; and

transferring via said common switch, said acquired at least one second identifier

of said first blade server to at least said second blade server.

8. (Currently Amended) The method according to claim 1, comprising

broadcasting at least a said header portion of said at least one received packet via said

common switch.

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 (Previously Presented) The method according to claim 1, comprising receiving a broadcast containing said at least one received packet.

receiving a proadcast containing said at least one received packet.

10. (Currently Amended) The method according to claim 1, comprising

receiving at least one packet from said second blade server and transferring via said

common switch, said at least at header portion of said at least one packet received from

said second blade server to at least one of said first blade server and a third blade

server.

11. (Currently Amended) A machine-readable storage having stored thereon.

a computer program having at least one code section for communicating information in

a server, the at least one code section being executable by a machine for causing the

machine to perform steps comprising:

receiving at a common switch, at least one packet from a first blade server of a

plurality of blade servers, wherein said at least one packet is designated for at least a

second blade server of said plurality of blade servers, and at least two of which wherein

said first blade server and said at least a second blade server are coupled to [[a]] said

common switch via a common bus:

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determining at least one identifier associated with at least a second blade server

based on at least a portion of said received at least one packet; and determining at least

a first identifier identifying said common switch, a second identifier identifying said first

blade server, and at least a third identifier identifying said second blade server, wherein

said first, second and third identifiers are located within a header portion of said

received at least one packet; and

routing via said common switch, based on said determined at least one identifier,

at least a portion of said at least one received packet to at least said second blade

server, based on said determined first, second and third identifiers from said header

portion of said at least one received packet.

12. (Currently Amended) The machine-readable storage according to claim

11, comprising code for transferring said at least a header portion of said at least one

received packet to said at least said second blade server via said common switch.

13. (Currently Amended) The machine-readable storage according to claim

11, wherein said common switch comprises a switch blade coupled to said common

bus, and wherein said machine-readable storage comprises code for controlling said

routing of said at least a header portion of said received packet by said switch blade

routing of said at least a <u>neader</u> portion of said received packet by said switch blac

coupled to said common bus.

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14. (Currently Amended) The machine-readable storage according to claim

[[13]]11, comprising determining at least one identifier of said switch bladewherein said

common bus comprises a backplane.

15. (Currently Amended) The machine-readable storage according to claim

[[14]]11, comprising code for determining at least one identifier of said first blade

serverwherein said common switch comprises a bus transceiver and a bus controller.

16. (Currently Amended) The machine-readable storage according to claim

[[15]]11, wherein each of said first, second, and third identifiers identifier of said first

blade server, said identifier of said second blade server, and said identifier of said

switch blade each-comprises one or both of a MAC address and/or an IP address.

17. (Currently Amended) The machine-readable storage according to claim

11, comprising:

code for acquiring at least one said second identifier of said first blade server; and

transferring via said common switch, said acquired at least onesecond identifier

of said first blade server to at least said second blade server.

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18. (Currently Amended) The machine-readable storage according to claim

11, comprising code for broadcasting at least a said header portion of said at least one

received packet via said common switch.

19. (Previously Presented) The machine-readable storage according to claim

11, comprising code for receiving a broadcast containing said at least one received

packet.

20. (Currently Amended) The machine-readable storage according to claim

11, comprising code for receiving at least one packet from said second blade server and

transferring via said common switch, said at least at header portion of said at least one

packet received from said second blade server to at least one of said first blade server

and a third blade server.

21. (Currently Amended) A system for communicating information in a server,

the system comprising:

at least one processor that receives at a common switch, at least one packet

from a first blade server of a plurality of blade servers, wherein said at least one packet

is designated for at least a second blade server of said plurality of blade servers, and at

least two of whichwherein said first blade server and said at least a second blade server

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are coupled to [[a]] said common switch via a common bus;

said at least one processor determines at least one identifier associated with at

least a second blade server based on at least a portion of said received at least one

packet; and at least a first identifier identifying said common switch, a second identifier

identifying said first blade server, and at least a third identifier identifying said second

blade server, wherein said first, second and third identifiers are located within a header

portion of said received at least one packet; and

said at least one processor routes via said common switch, based on said

determined at least one identifier, at least a portion of said at least one received packet

to at least said second blade server, based on said determined first, second and third

identifiers from said header portion of said at least one received packet.

22. (Currently Amended) The system according to claim 21, wherein said at

least one processor transfers said at least a $\underline{\text{header}}$ portion of said at least one received

packet to said at least said second blade server via said common switch.

23. (Currently Amended) The system according to claim 21, wherein said

common switch comprises a switch blade coupled to said common bus, and wherein

said at least one processor controls said routing of said at least a header portion of said

received packet by said switch blade coupled to said common bus.

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24. (Currently Amended) The system according to claim [[23]]21, wherein said

at least one processor determines at least one identifier of said switch bladesaid

common bus comprises a backplane.

25. (Currently Amended) The system according to claim [[24]]21, wherein said

at least one processor determines at least one identifier of said first blade serverwherein

said common switch comprises a bus transceiver and a bus controller.

26. (Currently Amended) The system according to claim [[25]]21, wherein

each of said first, second, and third identifiers identifier of said first blade server, said

identifier of said second blade server, and said identifier of said switch blade each

comprises one or both of a MAC address and/or an IP address.

27. (Currently Amended) The system according to claim 21, wherein said at

least one processor:

acquires at least onesaid second identifier of said first blade server; and

transferring via said common switch, said acquired at least one second identifier

of said first blade server to at least said second blade server.

28. (Currently Amended) The system according to claim 21, wherein said at

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least one processor broadcasts at least a said header portion of said at least one received packet via said common switch.

29. (Previously Presented) The system according to claim 21, wherein said at

least one processor receives a broadcast containing said at least one received packet.

30. (Currently Amended) The system according to claim 21, wherein said at

least one processor receives at least one packet from said second blade server and

transfers via said common switch, said at least at header portion of said at least one

packet received from said second blade server to at least one of said first blade server

and a third blade server.